# **CR6** Specifications



Electrical specifications are valid over a -40 to +70 °C, noncondensing environment, unless otherwise specified. Extended electrical specifications (noted as XT in specifications) are valid over a -55 to +85 °C non-condensing environment. Recalibration is recommended every three years. Critical specifications and system configuration should be confirmed with Campbell Scientific before purchase.

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# System specifications

**Processor**: Renesas RX63N (32-bit with hardware FPU, running at 100 MHz)

## Memory:

- Total onboard: 128 MB of flash + 4 MB battery-backed SRAM
  - Data storage: 4 MB SRAM + 72 MB flash (extended data storage automatically used for auto-allocated Data Tables not being written to a card)
  - ° CPU drive: 30 MB flash
  - OS load: 8 MB flash
  - ° Settings: 1 MB flash
  - Reserved (not accessible): 10 MB flash
- Data storage expansion: Removable microSD flash memory, up to 16 GB

## NOTE:

CR6 dataloggers with serial numbers less than 7502 have the following memory specifications:

CPU Drive / Programs: 1 MB flash USR Drive / Data: 4 MB SRAM (battery backed)

Program Execution Period: 1 ms to 1 day

Real-Time Clock:

- Battery backed while external power is disconnected
- Resolution: 1 ms
- Accuracy: ±3 min. per year, optional GPS correction to ±10 µs; 5.7 ppm

Wiring Panel Temperature: Measured using a thermistor, located on the analog board.

# Physical specifications

**Dimensions**:  $21.0 \times 10.2 \times 5.6$  cm ( $8.3 \times 4.0 \times 2.2$  in); additional clearance required for cables, wires, and antennas.

## Weight/Mass:

- CR6: 0.42 kg (0.92 lb)
- CR6-WIFI: 0.50 kg (1.10 lb)
- CR6-RF451/RF452: 0.52 kg (1.15 lb)
- CR6-RF407/412/422/427: 0.51 kg (1.13 lb)

**Case Material**: High-impact-resistant polycarbonate and UV-resistant TPE, recycle code 7

Dust Plug Material: UV-resistant TPE

## **Power requirements**

**Protection**: Power inputs are protected against surge, overvoltage, over-current, and reverse power. IEC 61000-4 Class 4 level.

CHG Terminals:

- Input Voltage: 16 to 32 VDC
- Input Current Limit at 12 VDC:
  - 1.2 A @ 20 °C maximum

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## BAT Terminals:

- 10 to 16 VDC
- 19 VDC sustained voltage limit without damage; transient voltage protected
- 2.5 A max current at 12 VDC at 20° C

#### External Batteries:

- Float charge on BAT terminal
- 12 VDC
- Valve-regulated, lead-acid (VRLA)
- 2 to 24 Ah battery typical

Vehicle Power Connection: When primary power is pulled from the vehicle power system, a second power supply OR charge regulator may be required to overcome the voltage drop at vehicle start-up.

**USB Power:** Functions that will be active with USB 5 VDC include sending programs, adjusting data logger settings, and making some measurements. If USB is the only power source, then the CS I/O port and the 12V and SW12 terminals will not be operational.

**Internal Lithium Battery**: AA, 2.4 Ah, 3.6 VDC (Tadiran TL 5903/S) for battery-backed SRAM and clock. 3-year life with no external power source.

#### Average Current Drain:

Assumes 12 VDC on BAT terminals — add 2 mA if using CHG terminals.

- **Idle**: <1 mA
- Active 1 Hz Scan: 3 mA
- Active 20 Hz Scan: 67 mA
- Serial (RS-232/RS-485): Active + 25 mA
- Ethernet Power Requirements:
  - Ethernet 1 Minute: Active + 1 mA
  - Ethernet Idle: Active + 4 mA
  - Ethernet Link: Active + 47 mA

## Wi-Fi Additional Current Contribution at 12 VDC:

| Mode              | Wi-Fi Option                    |
|-------------------|---------------------------------|
| Client Mode       | 7 mA idle, 70 mA communicating  |
| Access Point Mode | 62 mA idle, 70 mA communicating |
| Sleep             | <1 mA                           |

## RF Average Additional Current Contribution at 12 VDC:

|                          | -RF407, -RF412,<br>-RF427 | -RF422 | -RF451/RF452       |
|--------------------------|---------------------------|--------|--------------------|
| Transmit                 | < 80 mA                   | 20 mA  | 650 mA,<br>maximum |
| Idle On                  | 12 mA                     | 9.5 mA | 15 mA,<br>maximum  |
| Idle 0.5 s<br>Power Mode | 4 mA                      | 3.5 mA | NA                 |

|                        | -RF407, -RF412,<br>-RF427 | -RF422 | -RF451/RF452 |
|------------------------|---------------------------|--------|--------------|
| Idle 1 s Power<br>Mode | 3 mA                      | 2 mA   | NA           |
| Idle 4 s Power<br>Mode | 1.5 mA                    | 1.5 mA | NA           |

# Power output specifications

# System power out limits (when powered with 12 VDC)

| Temperature (°C)                                    | Current limit <sup>1</sup> (A) |  |  |
|---|--------------------------------|--|--|
| -40°  | 3.88                           |  |  |
| 0°  | 2.98                           |  |  |
| 20°   | 2.50                           |  |  |
| 50°   | 1.80                           |  |  |
| 70°   | 1.35                           |  |  |
| 85°   | 1.00                           |  |  |
| <sup>1</sup> Limited by self-resetting thermal fuse |                                |  |  |

## 12 V and SW12 V power output terminals

12V, SW12-1, and SW12-2: Provide unregulated 12 VDC power with voltage equal to the Power Input supply voltage. These are disabled when operating on USB power only.

| SW12 current limits                            |      |  |  |
|--|------|--|--|
| Temperature (°C) Current limit <sup>1</sup> (m |      |  |  |
| -40°   | 1600 |  |  |
| 0°   | 1290 |  |  |
| 20°  | 1100 |  |  |
| 50°  | 830  |  |  |
| 70° 640  |      |  |  |
| 85° 500  |      |  |  |
| <sup>1</sup> Thermal fuse hold current.        |      |  |  |

## U and C as power output

- C Terminals:
  - Output Resistance (R<sub>o</sub>): 150 Ω
  - 5 V Logic Level Drive Capacity: 10 mA @ 3.5 VDC
  - 3.3 V Logic Level Drive Capacity: 10 mA @ 1.8 VDC

• U Terminals:

|   | U odd<br>terminals | U even<br>terminals |  |
|---|--------------------|---------------------|--|
| Voltage Excitation Max<br>Current @ ±2500 mV  | ±25 mA             | ±25 mA              |  |
| Current Excitation Max<br>Current   | ±2500 μΑ           | ±2500 μΑ            |  |
| 5 V Logic Level Output<br>Resistance (R <sub>o</sub> )                                  | 75 Ω               | 150 Ω               |  |
| 5 V Logic Level Max Current<br>@ 3.5 V  | 20 mA              | 10 mA               |  |
| 3.3 V Logic Level Output<br>Resistance (R <sub>o</sub> )                                | 73 Ω               | 145 Ω               |  |
| 3.3 V Logic Level Max<br>Current @1.85 V  | 20 mA              | 10 mA               |  |
| U odd terminals: U1, U3, U5, U7, U9, U11.<br>U even terminals: U2, U4, U6, U8, U10, U12 |                    |                     |  |

# CS I/O pin 1

## 5 V Logic Level Max Current: 200 mA

## Voltage and current excitation specifications

A 12-bit DAC produces voltage and current excitation. When used for resistance measurement, excitation is active only during measurement.

Voltage excitation

Terminals: U1 - U12

Range: ±2500 mV

Resolution: 0.6 mV

## Voltage Excitation Absolute Accuracy:

- 0 to 40 °C: ±(0.1% of setting + 1.2 mV)
- -40 to 70 °C: ±(0.1% of setting + 1.5 mV)
- -55 to 85 °C (XT): ±(0.1% of setting + 1.6 mV)

Maximum Source or Sink Current: ±25 mA

## Current excitation

Terminals: U1 - U12

Range: ±2.5 mA

Resolution: 0.6 µA

## Current Excitation Absolute Accuracy):

- 0 to 40 °C: ±(0.11% of setting + 2.0 μA)
- -40 to 70 °C: ±(0.12% of setting + 2.5 μA)
- -55 to 85 °C (XT): ±(0.13% of setting + 3.0 μA)

## Compliance Voltage: ±5 V

## Analog measurement specifications

12 universal (U) terminals individually configurable for voltage, thermocouple, thermistor, current loop, ratiometric, static

vibrating wire, and period average measurements, using a 24bit ADC. One channel at a time is measured.

## Voltage measurements

## Terminals:

- Differential Configuration (H/L): U1/U2- U11/U12
- Single-Ended Configuration: U1 U12

Input Resistance: 20 G $\Omega$  typical

Input Voltage Limits: ±5 V

Sustained Input Voltage without Damage: ±20 VDC

## DC Common Mode Rejection:

- >120 dB with input reversal
- $\geq$  86 dB without input reversal

Normal Mode Rejection: > 70 dB @ 60 Hz

Input Current @ 25 °C: ±2 nA typical

Filter First Notch Frequency ( $f_{N1}$ ) Range: 5 Hz to 93 kHz (user specified)

## Analog Range and Resolution:

|   |                            |             | ential<br>input<br>ersal | Single-ended<br>and differential<br>without input<br>reversal |                   |
|---|----------------------------|-------------|--------------------------|---|-------------------|
| Notch<br>frequency<br>(f <sub>N1</sub> ) (Hz) | Range <sup>1</sup><br>(mV) | RMS<br>(µV) | Bits <sup>2</sup>        | RMS<br>(µV)   | Bits <sup>2</sup> |
| 15000   | ±5000                      | 20.0        | 19                       | 30.0  | 18                |
|   | ±1000                      | 4.0         | 19                       | 5.5   | 18                |
|   | ±200                       | 1.6         | 18                       | 1.8   | 17                |
| 50/60 <sup>3</sup>                            | ±5000                      | 1.2         | 23                       | 5.0   | 20                |
|   | ±1000                      | 0.24        | 23                       | 1.1   | 20                |
|   | ±200                       | 0.10        | 22                       | 0.24  | 20                |
| 5   | ±5000                      | 0.60        | 24                       | 4.9   | 20                |
|   | ±1000                      | 0.12        | 24                       | 1.0   | 20                |
|   | ±200                       | 0.05        | 23                       | 0.22  | 20                |

 $^1\,\rm Range$  overhead of  $\sim\!5\%$  on all ranges guarantees that full-scale values will not cause over range

<sup>2</sup> Typical effective resolution (ER) in bits; computed from ratio of fullscale range to RMS resolution.

 $^3$  50/60 corresponds to rejection of 50 and 60 Hz ac power mains noise.

Accuracy (does not include sensor or measurement noise):

- 0 to 40 °C:  $\pm$  (0.04% of measurement + offset)
- -40 to 70 °C: ±(0.06% of measurement + offset)
- -55 to 85 °C (XT): ±(0.08% of measurement + offset)

Voltage Measurement Accuracy Offsets:

|               | Typical offset (µV RMS)                |   |  |  |
|---------------|--|---|--|--|
| Range<br>(mV) | Differential<br>with input<br>reversal | Single-ended or<br>differential<br>without input reversal |  |  |
| ±5000         | ±10                                    | ±40   |  |  |
| ±1000         | ±5                                     | ±12   |  |  |
| ±200          | ±2                                     | ±6  |  |  |

**Measurement Settling Time**: 20 µs to 600 ms; 500 µs default **Multiplexed Measurement Time**:

Measurement time = INT(multiplexed measurement time • (reps+1) + 2ms

|  | Differential<br>with input<br>reversal | Single-ended or<br>differential<br>without input reversal |  |  |
|--|--|---|--|--|
| Example fN1 <sup>1</sup><br>(Hz)                   | Time <sup>2</sup> (ms)                 | Time <sup>2</sup> (ms)                                    |  |  |
| 15000  | 2.8                                    | 1.4   |  |  |
| 60   | 36                                     | 18.1  |  |  |
| 50   | 42.07                                  | 21.3  |  |  |
| 5  | 402.7                                  | 201.4   |  |  |
| <sup>1</sup> Notch frequency (1/integration time). |  |   |  |  |

<sup>2</sup> Default settling time of 500 µs used.

## Resistance measurement specifications

The data logger makes ratiometric-resistance measurements for four- and six-wire full-bridge circuits and two-, three-, and four-wire half-bridge circuits using voltage excitation or for direct resistance measurements using current excitation. Excitation polarity reversal is available to minimize dc error.

## Accuracy:

Assumes input reversal for differential measurements **RevDiff** and excitation reversal **RevEx** for excitation voltage <1000 mV and excitation current < 1 mA. Does not include bridge resistor errors or sensor and measurement noise.

- 0 to 40 °C: ±(0.02% of voltage measurement + offset)
- -40 to 70 °C: ±(0.025% of voltage measurement + offset)
- -55 to 85 °C (XT): ±(0.03% of voltage measurement + offset)

## Terminals: U1-U12

Accuracy:  $\pm (0.01\%$  of measurement + resolution), where resolution is 0.13  $\mu s$  divided by the number of cycles to be measured

## Ranges:

• Minimum signal centered around specified period average threshold.

- Maximum signal centered around data logger ground.
- Maximum frequency = 1/(2 \* [minimum pulse width]) for 50% duty cycle signals

| Gain<br>code<br>op-<br>tion | Volt-<br>age<br>gain | Min-<br>imum<br>peak to<br>peak<br>signal<br>(mV) | Max-<br>imum<br>peak to<br>peak<br>signal<br>(V) | Min-<br>imum<br>pulse<br>width<br>(µs) | Max-<br>imum<br>fre-<br>quency<br>(kHz) |
|-----------------------------|----------------------|---|--|--|---|
| 0                           | 1                    | 500   | 10   | 2.5                                    | 200                                     |
| 1                           | 2.5                  | 50  | 2  | 10                                     | 50                                      |
| 2                           | 12.5                 | 10  | 2  | 62                                     | 8                                       |
| 3                           | 64                   | 2   | 2  | 100                                    | 5                                       |

## Static vibrating wire measurement specifications

Up to 6 static vibrating wire measurements without thermistor measurements, or up to 3 static vibrating wire measurements with thermistor measurements. A U terminal pair both excites and measures vibrating wire transducers. Logarithmic sine-wave-frequency excitation is adjustable up to  $\pm$ 6 V (12 V peak-to-peak), programmable from 100 Hz to 6.5 kHz, then followed by frequency domain measurements, one at a time.

Terminals: U1 - U12

Input Resistance: 4.75 kΩ

Measurement Type: Differential voltage

Range: ±200 mV

Accuracy: ±0.013% of reading

Resolution: 0.001 Hz RMS

**Measurement Speed** (vibrating wire and thermistor combined): < 1 s

## Thermistor measurement specifications

6 U terminal pairs can be configured to measure two-wire thermistors directly using an on-board 5 k $\Omega$  resistor to complete the bridge. The U terminal pair both excites and measures the thermistor.

Terminals: U1 - U12

Input Resistance: 5 k $\Omega$  ±0.1%, 10 ppm/°C completion resistor

Measurement Type: Single-ended voltage

Range: ±5000 mV

**Resolution**: 0.001 Ω RMS

Accuracy: ±0.25% of reading

## Current-loop measurement specifications

The data logger makes current-loop measurements by measuring across a current-sense resistor associated with the RS-485 resistive ground terminal.

## NOTE:

Resistance to ground input for non-isolated 0-20 mA and 4-20 mA current loop measurements is available in CR6 data loggers with serial numbers 7502 and greater.

## Terminal: RG

Maximum Input Voltage: ±16 V

Resistance to Ground: 101  $\Omega$ 

Current Measurement Shunt Resistance: 10  $\Omega$ 

Maximum Current Measurement Range: ±80 mA

Absolute Maximum Current: ±160 mA

**Resolution**: ≤ 20 nA

## Accuracy:

- 0 to 40 °C: ±0.14% of reading
- -40 to 70 °C: ±0.26% of reading

## Pulse measurement specifications

The data logger can measure switch closure or high-frequency pulse signals on C and U terminals. Terminals are configured as pairs with options for pull-up or pull-down. Even-numbered U terminals can be configured as low-level AC inputs. Each terminal has its own independent 24-bit counter. Terminal pairs are U1 and U2, U3 and U4, through U11 and U12.

## NOTE:

Conflicts can occur when a control port pair is used for different instructions (TimerInput(), PulseCount(), SDI12Recorder(), WaitDigTrig()). For example, if C1 is used for SDI12Recorder(), C2 cannot be used for TimerInput(), PulseCount(), or WaitDigTrig().

Pulse Event: Transition from logic low to logic high.

| Terminal Pair Configuration | Logic Low | Logic High |
|-----------------------------|-----------|------------|
| 5 V                         | ≤ 1.5 V   | ≥ 3.5 V    |
| 3.3 V                       | ≤ 0.8 V   | ≥ 2.0 V    |

Sustained Input Voltage without Damage:  $\pm 20$  VDC

Maximum Counts Per Scan: 224

Input Resistance: 5 kΩ

Accuracy: ±(0.02% of reading + 1/scan)

## Low-level AC input

Terminals: U2, U4, U6, U8, U10, U12

## NOTE:

When an even numbered U terminal (for example, U2) is used for low-level AC pulse counting, its paired odd numbered terminal (U1 in this example) can be used only for switch-closure.

DC-offset rejection: Internal AC coupling eliminates DC-offset voltages up to  $\pm 0.05$  VDC

## Input Hysteresis: 12 mV at 1 Hz

#### Low-Level AC Pulse Input Ranges for U Terminals:

| Sine wave (mV RMS) | Range (Hz)    |
|--------------------|---------------|
| 20                 | 1.0 to 20     |
| 200                | 0.5 to 200    |
| 2000               | 0.3 to 10,000 |
| 5000               | 0.3 to 20,000 |

## Switch closure input

Terminals: C1-C4, U1-U12

 $\mbox{Resistance}:$  Configurable in terminal pairs with 100 k $\Omega$  pull-up or pull-down

Maximum Input Frequency: 150 Hz

Minimum Switch Closed Time: 5 ms

Minimum Switch Open Time: 6 ms

Maximum Bounce Time: 1 ms open without being counted

Software Debounce Time: 3.3 ms

High-frequency input

Terminals: C1-C4, U1-U12

 $\mbox{Resistance}:$  Configurable in terminal pairs with 100 k $\Omega$  pull-up or pull-down

Typical Wave Form: 5 or 3.3 VDC square wave

Maximum Input Frequency: 1 MHz

# Digital input/output specifications

Terminals configurable for digital input and output (I/O) including status high/low, pulse width modulation, external interrupt, edge timing, switch closure pulse counting, high-frequency pulse counting, plus UART<sup>1</sup>, RS-232<sup>2</sup>, RS-422<sup>3</sup>, RS-485<sup>4</sup>, SDM<sup>5</sup>, SDI-12<sup>6</sup>, I2C<sup>7</sup>, and SPI<sup>8</sup> serial-communications

<sup>&</sup>lt;sup>1</sup>Universal Asynchronous Receiver/Transmitter for asynchronous serial communications.

<sup>&</sup>lt;sup>2</sup>Recommended Standard 232. A loose standard defining how two computing devices can communicate with each other. The implementation of RS-232 in Campbell Scientific data loggers to computer communications is quite rigid, but transparent to most users. Features in the data logger that implement RS-232 communications with smart sensors are flexible.

<sup>&</sup>lt;sup>3</sup>Communications protocol similar to RS-485. Most RS-422 sensors will work with RS-485 protocol.

<sup>&</sup>lt;sup>4</sup>Recommended Standard 485. A standard defining how two computing devices can communicate with each other.

<sup>&</sup>lt;sup>5</sup>Synchronous Device for Measurement. A processor-based peripheral device or sensor that communicates with the data logger via hardwire over a short distance using a protocol proprietary to Campbell Scientific.

<sup>&</sup>lt;sup>6</sup>Serial Data Interface at 1200 baud. Communications protocol for transferring data between the data logger and SDI-12 compatible smart sensors.

<sup>&</sup>lt;sup>7</sup>Inter-Integrated Circuit is a multi-controller, multi-peripheral, packet switched, single-ended, serial computer bus.

<sup>&</sup>lt;sup>8</sup>Serial Peripheral Interface - a clocked synchronous interface, used for short distance communications, generally between embedded devices.

functions. Terminals are configurable in pairs for 5 V or 3.3 V logic for some functions.

## NOTE:

Conflicts can occur when a control port pair is used for different instructions (TimerInput(), PulseCount(), SDI12Recorder(), WaitDigTrig()). For example, if C1 is used for SDI12Recorder(), C2 cannot be used for TimerInput(), PulseCount(), or WaitDigTrig().

Sustained Logic Input Voltage without Damage: ±20 VDC

## Logic Levels and Drive Current:

| Terminal pair configuration | 5 V source   | 3.3 V source  |
|-----------------------------|--------------|---------------|
| Logic low                   | ≤ 1.5 V      | ≤ 0.8 V       |
| Logic high                  | ≥ 3.5 V      | ≥ 2.5 V       |
| C1 - C4                     | 10 mA @ 3.5V | 10 mA @ 1.85V |
| U odd                       | 20 mA @ 3.5V | 20 mA @ 1.85V |
| U even                      | 10 mA @ 3.5V | 10 mA @ 1.85V |

## Edge timing

Terminals: C1-C4, U1-U12

Maximum Input Frequency: ≤ 1 kHz

Resolution: 520 ns

## Edge counting

Terminals: C1-C4, U1-U12

Maximum Input Frequency:  $\leq$  2.2 kHz

## Quadrature input

**Terminals**: C1-C4 and U1-U12 can be configured as digital pairs to monitor the two sensing channels of an encoder.

Maximum Frequency: 2.5 kHz

Resolution: 31.25 µs or 32 kHz

Minimum Pulse Width: 10 µs

## Pulse-width modulation

Modulation Voltage: Logic high

Maximum Period: 128 seconds

Resolution:

- 0 to ≤ 5 ms: 1/12 MHz or 83.33 ns
- > 5 to < 300 ms: 187.62 kHz or 5.33  $\mu s$
- > 300 ms: 1/32 kHz or 31.25 μs

## Communications specifications

**Ethernet Port**: RJ45 jack, 10/100Base Mbps, full and half duplex, Auto-MDIX, magnetic isolation, and TVS surge protection.

Internet Protocols: Ethernet, PPP, RNDIS, ICMP/Ping, Auto-IP (APIPA), IPv4, IPv6, UDP, TCP, TLS (v1.2), DNS, DHCP, SLAAC, Telnet, HTTP(S), SFTP, FTP(S), POP3/TLS, NTP, SMTP/TLS, SNMPv3, CS I/O IP, MQTT Additional Protocols: CPI, PakBus, PakBus Encryption, SDM, SDI-12, Modbus RTU / ASCII / TCP, DNP3, custom user definable over serial, NTCIP, NMEA 0183, I2C, SPI

USB Device: Micro-B device for computer connectivity

**CS I/O**: 9-pin D-sub connector to interface with Campbell Scientific CS I/O peripherals.

**0 – 5 V Serial** (U1 to U12, C1 to C4): Eight independent TX/RX pairs

**SDI-12** (C1, C3, U1, U3, U5, U7, U9, U11): Eight independent SDI-12 compliant terminals are individually configured and meet SDI-12 Standard v 1.4.

RS-485 (C1 to C4): Two half duplex

RS-422 (C1 to C4): One full duplex or two half duplex

**RS-232/CPI**: Single RJ45 module port that can operate in one of two modes: CPI or RS-232. CPI interfaces with Campbell Scientific CDM measurement peripherals and sensors. RS-232 connects, with an adapter cable, to computer, sensor, or communications devices serially.

**CPI**: One CPI bus. Up to 1 Mbps data rate. Synchronization of devices to 5  $\mu$ S. Total cable length up to 610 m (2000 ft). Up to 20 devices. CPI is a proprietary interface for communications between Campbell Scientific data loggers and Campbell Scientific CDM peripheral devices. It consists of a physical layer definition and a data protocol.

## Antenna Connection:

- Wi-Fi
- SS 900 MHz
- 2.4 GHz

Wireless: Wi-Fi, VHF, UHF, spread spectrum, ELOS

Hardwired: Multi-drop, short haul, RS-232, fiber optic

Satellite: GOES, Argos, Inmarsat Hughes, Irridium

## Wi-Fi option specifications

WLAN (Wi-Fi) (CR6-WIFI only)

Maximum Possible Over-the-Air Data Rates: <11 Mbps over 802.11b, <54 Mbps over 802.11g, <72 Mbps over 802.11n

Operating Frequency: 2.4 GHz, 20 MHz bandwidth

Antenna Connector: Reverse Polarity SMA (RPSMA)

**Antenna** (shipped with data logger): Unity gain (0 dBd), 1/2 wave whip, omnidirectional. Features an articulating knuckle joint that can be oriented vertically or at right angles

Supported Technologies: 802.11 b/g/n, WPA/WPA2-Personal, WPA/WPA2-Enterprise Security, WEP

Client Mode: WPA/WPA2-Personal and Enterprise, WEP

Access Point Mode: WPA2-Personal

Receive Sensitivity: -97 dBm

## RF radio option specifications

Antenna Terminal: Reverse Polarity SMA (RPSMA)

## Radio Type

- **RF407, RF412, RF427**, and **RF451/RF452**: Frequency-Hopping Spread-Spectrum (FHSS)
- **RF407, RF412**, and **RF427**: Frequency-Hopping Spread-Spectrum (FHSS)
- **RF422**: SRD860 Radio with Listen Before Talk (LBT) and Automatic Frequency Agility (AFA)

## Frequency

- RF407: 902 to 928 MHz (US, Canada)
- RF412: 915 to 928 MHz (Australia, New Zealand)
- **RF422**: 863 to 870 MHz (Europe, Middle East, and Africa)
- RF427: 902 to 907.5 MHz/915 to 928 MHz (Brazil)
- RF451/RF452: 902 to 928 MHz

## Transmit Power Output (software selectable)

- RF407 and RF412: 5 to 250 mW
- **RF422**: 2 to 25 mW
- **RF427**: 5 to 250 mW
- **RF451/RF452**: 10 mW to 1,000 mW

## **Channel Capacity**

- **RF407**: Eight 25-channel hop sequences sharing 64 available channels.
- **RF412**: Eight 25-channel hop sequences sharing 31 available channels.
- **RF422**: Ten 30-channel hop sequences (default), software configurable to meet local regulations; 10 sequences for reducing interference through channel hop.
- **RF427**: Eight 25-channel hop sequences sharing 43 available channels.
- **RF451/RF452**: 50 to 112 user-selectable channels for a given network.

## **Receive Sensitivity**

- RF407, RF412, and RF427: -101 dBm
- **RF422**: -106 dBm
- RF451/RF452:
  - $^\circ~$  –108 dBm at 115.2 kbps for 10^-4 BER
  - $^\circ~-$  103 dBm at 153.6 kbps for 10^-4 BER

## RF Data Rate

- RF407, RF412, and RF427: 200 kbps
- **RF422**: 10 kbps
- RF451/RF452: 115.2 or 153.6 kbps

## Maximum nodes in network

- RF407, RF412, and RF427: 50
- **RF422**: 20
- RF451/RF452: 4 repeaters

# Standards compliance specifications

View compliance and conformity documents at www.campbellsci.com/cr6  $\square$ .

Shock and Vibration: MIL-STD 810G methods 516.6 and 514.6 Protection: IP50

## EMI and ESD protection:

- Immunity: Meets or exceeds following standards:
  - ESD: per IEC 61000-4-2; ±15 kV air, ±8 kV contact discharge
  - Radiated RF: per IEC 61000-4-3; 10 V/m, 80-1000 MHz
  - **EFT**: per IEC 61000-4-4; 4 kV power, 4 kV I/O
  - Surge: per IEC 61000-4-5; 4 kV power, 4kV I/O
  - Conducted RF: per IEC 61000-4-6; 10 V power, 10 V I/O
- Emissions and immunity performance criteria available on request.

## **RF407** Option

- United States FCC Part 15.247: MCQ-XB900HP
- Industry Canada (IC): 1846A-XB900HP
- Mexico IF: RCPDIXB15-0672-A1

## **RF412** Option

- ACMA RCM
- United States FCC Part 15.247:
- MCQ-XB900HP
- Industry Canada (IC): 1846A-XB900HP

# **RF422 Option**: View EU Declaration of Conformity at www.campbellsci.com/cr6 $\square$ <sup>7</sup>.

**RF427 Option**: Brazil ANATEL standards in Resolution No. 506: 08335-17-10644. View the RF427 Brazilian Certificate of Conformity at www.campbellsci.com/cr6 <sup>I</sup>.

## RF452 Option:

- United States FCC ID: KNYMM3
- Industry Canada (IC): 2329B-MM3

## RF451 Option:

- United States FCC ID: KNYAMM0921TT
- Industry Canada (IC): 2329B-AMM0921TT

## WIFI Option

- United States FCC ID: XF6-RS9113SB
- Industry Canada (IC): 8407A-RS9113SB

## NOTE:

The user is responsible for emissions if changing the antenna type or increasing the gain.

## Warranty

**Standard**: Three years against defects in materials and workmanship.

**Extended** (optional): An additional four years, bringing the total to seven years.

# Terminal functions

| Analog input terminal functions  |              |              |              |              |              |              |              |              |              |              |              |              |              |
|----------------------------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|
|                                  | U1           | U2           | U3           | U4           | U5           | U6           | U7           | U8           | U9           | U10          | U11          | U12          | RG           |
| Single-Ended Voltage             | $\checkmark$ |              |
| Differential Voltage             | Н            | L            | Н            | L            | Н            | L            | Н            | L            | Н            | L            | Н            | L            |              |
| Ratiometric/Bridge               | $\checkmark$ |              |
| Vibrating Wire (Static, VSPECT®) | $\checkmark$ |              |              |
| Vibrating Wire with Thermistor   |              | `            | /            |              | $\checkmark$ |              |              |              | $\checkmark$ |              |              |              |              |
| Thermistor                       | · ·          | /            | $\checkmark$ |              |              |
| Thermocouple                     | $\checkmark$ |              |
| Current Loop                     |              |              |              |              |              |              |              |              |              |              |              |              | $\checkmark$ |
| Period Average                   | $\checkmark$ |              |

| Pulse counting terminal functions |              |              |              |              |              |              |              |              |              |              |              |              |              |
|-----------------------------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|--------------|
|                                   | U1           | U2           | U3           | U4           | U5           | U6           | U7           | U8           | U9           | U10          | U11          | U12          | C1-C4        |
| Switch-Closure                    | $\checkmark$ |
| High Frequency                    | $\checkmark$ |
| Low-level AC                      |              | $\checkmark$ |              |

| Analog output terminal functions |              |  |  |  |
|----------------------------------|--------------|--|--|--|
|                                  | U1-U12       |  |  |  |
| Switched Voltage Excitation      | $\checkmark$ |  |  |  |
| Switched Current Excitation      | $\checkmark$ |  |  |  |

| Voltage output terminal functions  |              |              |              |              |              |
|--|--------------|--------------|--------------|--------------|--------------|
|  | U1-U12       | C1-C4        | 12V          | SW12-1       | SW12-2       |
| 3.3 VDC  | $\checkmark$ | $\checkmark$ |              |              |              |
| 5 VDC  | $\checkmark$ | $\checkmark$ |              |              |              |
| 12 VDC   |              |              | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| C and even numbered U terminals have limited drive capacity. Voltage levels are configured in pairs. |              |              |              |              |              |

| Communica               | tions to | ermina | l functi     | ons |              |      |              |     |              |      |              |     |              |      |              |     |                    |
|-------------------------|----------|--------|--------------|-----|--------------|------|--------------|-----|--------------|------|--------------|-----|--------------|------|--------------|-----|--------------------|
|                         | U1       | U2     | U3           | U4  | U5           | U6   | U7           | U8  | U9           | U10  | U11          | U12 | C1           | C2   | C3           | C4  | RS-<br>232/<br>CPI |
| SDI-12                  | √        |        | $\checkmark$ |     | $\checkmark$ |      | $\checkmark$ |     | $\checkmark$ |      | $\checkmark$ |     | $\checkmark$ |      | $\checkmark$ |     |                    |
| GPS Time<br>Sync        | PPS      | Rx     |              |     |              |      |              |     |              |      |              |     | Тx           | Rx   | Тx           | Rx  |                    |
| TTL<br>0-5 V            | Tx       | Rx     | Тx           | Rx  | Тx           | Rx   | Tx           | Rx  | Тх           | Rx   | Тх           | Rx  | Tx           | Rx   | Tx           | Rx  |                    |
| LVTTL<br>0-3.3 V        | Тx       | Rx     | Тx           | Rx  | Тх           | Rx   | Тx           | Rx  | Тх           | Rx   | Тх           | Rx  | Тx           | Rx   | Tx           | Rx  |                    |
| RS-232                  |          |        |              |     |              |      |              |     |              |      |              |     | Тx           | Rx   | Тx           | Rx  | $\checkmark$       |
| RS-485<br>(Half Duplex) |          |        |              |     |              |      |              |     |              |      |              |     | A-           | B+   | A-           | B+  |                    |
| RS-485<br>(Full Duplex) |          |        |              |     |              |      |              |     |              |      |              |     | Tx-          | Tx+  | Rx-          | Rx+ |                    |
| 12C                     | SCL      | SDA    | SCL          | SDA | SCL          | SDA  | SCL          | SDA | SCL          | SDA  | SCL          | SDA | SCL          | SDA  | SCL          | SDA |                    |
| SPI                     | SCLK     | COPI   | CIPO         |     | SCLK         | COPI | CIPO         |     | SCLKI        | COPI | CIPO         |     | SCLK         | COPI | CIPO         |     |                    |
| SDM                     | Data     | Clk    | Enabl        |     | Data         | Clk  | Enabl        |     | Data         | Clk  | Enabl        |     | Data         | Clk  | Enabl        |     |                    |
| CPI/<br>CDM             |          |        |              |     |              |      |              |     |              |      |              |     |              |      |              |     | $\checkmark$       |

| Digital I/O terminal functions |              |              |  |  |
|--------------------------------|--------------|--------------|--|--|
|                                | U1-U12       | C1-C4        |  |  |
| General I/O                    | $\checkmark$ | $\checkmark$ |  |  |
| Pulse-Width Modulation Output  | $\checkmark$ | $\checkmark$ |  |  |
| Timer Input                    | $\checkmark$ | $\checkmark$ |  |  |
| Interrupt                      | $\checkmark$ | $\checkmark$ |  |  |
| Quadrature                     | $\checkmark$ | $\checkmark$ |  |  |

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