
Mauna Loa Solar Observatory Observer's Log

Sat Oct 3 17:09:49 GMT 1998

Year: 98 Doy: 276

Observer: koon

WEATHER COMMENT: Sat Oct 3 17:10:14 GMT 1998

Clear sky, wind=5 mph from the SE, temp=49 F.

Sat Oct 3 17:11:10 GMT 1998 CHIP Startup--Initializing new tape

Sat Oct 3 17:16:41 GMT 1998 PICS Start Patrol

PSPT COMMENT: Sat Oct 3 17:48:19 GMT 1998

Judging from the email and logs it looks like we'll need to replace the mirror drive board as originally suspected, and that we can get some data in the meantime, so I started up the instrument.

CHIP COMMENT: Sat Oct 3 17:49:59 GMT 1998

The filter temperature is still not regulating properly, the controller is turned off to prevent damage to the filter. I'm not sure what needs to be done next so I won't observe with CHIP until I can go through the email messages more carefully.

Sat Oct 3 18:02:45 GMT 1998 PICS Flat

Sat Oct 3 18:04:14 GMT 1998 PICS End Flat

Sat Oct 3 19:02:45 GMT 1998 PICS Flat

Sat Oct 3 19:04:18 GMT 1998 PICS End Flat

Sat Oct 3 20:02:47 GMT 1998 PICS Flat

Sat Oct 3 20:04:16 GMT 1998 PICS End Flat

Sat Oct 3 21:02:54 GMT 1998 PICS Flat

Sat Oct 3 21:04:35 GMT 1998 PICS End Flat

Sat Oct 3 21:46:12 GMT 1998 PICS End Patrol

COMMENT: Sat Oct 3 22:18:42 GMT 1998

I was bumping the spar around quite today while I worked on the CHIP.

CHIP COMMENT: Sat Oct 3 22:35:45 GMT 1998

I removed the CHIP covers and checked the filter temperature sensor connections and cables. The cables are good physically and electrically, but the connection at the filter is flaky, resistance through the cable changed when I wiggled that connection. The connection at the temperature controller uses the same kind of connectors and it isn't flaky when wiggled. I'm not sure if this is the cause of the temperature problems we are having, but it makes sense that increased resistance through that cable could cause the controller to think the filter is too cold and therefore drive the filter temperature high. I'm leaving the covers loosely installed for insulation and so I can easily remove them tomorrow when I'll continue testing. I left the controller off all day and it will remain off overnight also.

COMMENT: Sat Oct 3 22:42:28 GMT 1998

TAPES:

PICS: P01311

CHIP: Down for troubleshooting and repair.
LOWL: L00546 in drive #0

Sat Oct 3 22:46:30 GMT 1998
MkIII

17_16.rawmk3	18_21.rawmk3	19_17.rawmk3	20_12.rawmk3	21_08.rawmk3
17_19.rawmk3	18_24.rawmk3	19_20.rawmk3	20_15.rawmk3	21_11.rawmk3
17_22.rawmk3	18_27.rawmk3	19_22.rawmk3	20_18.rawmk3	21_13.rawmk3
17_25.rawmk3	18_30.rawmk3	19_25.rawmk3	20_21.rawmk3	21_16.rawmk3
17_28.rawmk3	18_33.rawmk3	19_28.rawmk3	20_24.rawmk3	21_19.rawmk3
17_30.rawmk3	18_36.rawmk3	19_31.rawmk3	20_27.rawmk3	21_22.rawmk3
17_33.rawmk3	18_39.rawmk3	19_34.rawmk3	20_30.rawmk3	21_25.rawmk3
17_36.rawmk3	18_42.rawmk3	19_37.rawmk3	20_33.rawmk3	21_28.rawmk3
17_39.rawmk3	18_44.rawmk3	19_40.rawmk3	20_35.rawmk3	21_31.rawmk3
17_42.rawmk3	18_47.rawmk3	19_43.rawmk3	20_38.rawmk3	21_34.rawmk3
17_45.rawmk3	18_50.rawmk3	19_46.rawmk3	20_41.rawmk3	21_37.rawmk3
17_48.rawmk3	18_53.rawmk3	19_49.rawmk3	20_44.rawmk3	21_40.rawmk3
17_51.rawmk3	18_56.rawmk3	19_52.rawmk3	20_47.rawmk3	21_43.rawmk3
17_54.rawmk3	18_59.rawmk3	19_55.rawmk3	20_50.rawmk3	c17_57.rawmk3
18_00.rawmk3	19_02.rawmk3	19_57.rawmk3	20_53.rawmk3	c18_03.rawmk3
18_06.rawmk3	19_05.rawmk3	20_00.rawmk3	20_56.rawmk3	c18_09.rawmk3
18_12.rawmk3	19_08.rawmk3	20_03.rawmk3	20_59.rawmk3	
18_15.rawmk3	19_11.rawmk3	20_06.rawmk3	21_02.rawmk3	
18_18.rawmk3	19_14.rawmk3	20_09.rawmk3	21_05.rawmk3	