

-----  
Mauna Loa Solar Observatory Observer's Log  
-----

Tue Sep 25 16:39:19 GMT 2007

Year: 07 Doy: 268

Observer: stueben

WEATHER COMMENT: Tue Sep 25 16:48:35 GMT 2007

Clear, wind south 10-15 mph, temp 45F.

\_\_\_\_end\_\_\_\_

Tue Sep 25 16:49:41 GMT 2007 CHIP Start Patrol

Tue Sep 25 16:49:59 GMT 2007 PICS Start Patrol

Tue Sep 25 17:07:54 GMT 2007 MKIV Start Patrol

PSPT COMMENT: Tue Sep 25 17:46:13 GMT 2007

Observing.

\_\_\_\_end\_\_\_\_

Tue Sep 25 18:00:16 GMT 2007 PICS Flat

Tue Sep 25 18:01:56 GMT 2007 CHIP LSD

Tue Sep 25 18:02:37 GMT 2007 PICS End Flat

Tue Sep 25 18:02:45 GMT 2007 PICS ReStart Patrol

Tue Sep 25 18:03:39 GMT 2007 CHIP End LSD

Tue Sep 25 18:03:53 GMT 2007 CHIP BiasLSD

Tue Sep 25 18:04:51 GMT 2007 CHIP End BiasLSD

Tue Sep 25 18:05:00 GMT 2007 CHIP Bias

Tue Sep 25 18:05:52 GMT 2007 CHIP End Bias

Tue Sep 25 18:06:00 GMT 2007 CHIP ReStart Patrol

Tue Sep 25 18:25:30 GMT 2007 MKIV Start Cal

Tue Sep 25 18:44:11 GMT 2007 MKIV End Cal

Tue Sep 25 18:44:17 GMT 2007 MKIV Start Patrol

Tue Sep 25 18:50:25 GMT 2007 MKIV End Patrol

MKIV COMMENT: Tue Sep 25 18:54:13 GMT 2007

Stopped Mk4 to document ENV parameters on cpu and make changes on the replacement board so the boards are the same.

\_\_\_\_end\_\_\_\_

Tue Sep 25 19:51:48 GMT 2007 CHIP End Patrol

Tue Sep 25 19:51:51 GMT 2007 PICS End Patrol

Tue Sep 25 19:59:00 GMT 2007 CHIP Start Patrol

Tue Sep 25 19:59:12 GMT 2007 PICS Start Patrol

COMMENT: Tue Sep 25 20:01:11 GMT 2007

Reconfigured dome shutter.

\_\_\_\_end\_\_\_\_

\*\*PSPT PROBLEM\*\* Tue Sep 25 21:05:40 GMT 2007

Crashed while I was working on Mk4, killed and restarted.

\_\_\_\_end\_\_\_\_

WEATHER COMMENT: Tue Sep 25 21:38:54 GMT 2007

Orographic clouds are beginning to degrade images.

\_\_\_\_end\_\_\_\_

Tue Sep 25 22:00:59 GMT 2007 CHIP End Patrol

Tue Sep 25 22:01:06 GMT 2007 PICS End Patrol

WEATHER COMMENT: Tue Sep 25 22:01:50 GMT 2007

Clouds have become dense, instruments idled.

\_\_\_end\_\_\_

A NEW TAPE HAS BEEN PUT INTO KAIEE DLT DRIVE, Tue Sep 25 22:43:24 GMT 2007

MKIV COMMENT: Tue Sep 25 23:15:08 GMT 2007

Documented the ENV parameters on the working cpu. Changed parameters ton the replacement to match, but they do not hold through a power cycle. Since a test change on the working cpu holds through a power cycle I suspect the the NVRAM on the replacement is bad.

\_\_\_end\_\_\_

NICE CHIP IMAGE: 1752

NICE PICSDISC IMAGE: 1750

NICE PICSLIMB IMAGE: 1754

NICE MK4 IMAGE: 1752

Wed Sep 26 00:37:33 GMT 2007

MkIV

17_07.rawmk4	17_28.rawmk4	17_49.rawmk4	18_09.rawmk4	18_40.rawmk4
17_10.rawmk4	17_31.rawmk4	17_52.rawmk4	18_12.rawmk4	18_44.rawmk4
17_13.rawmk4	17_34.rawmk4	17_55.rawmk4	18_15.rawmk4	18_47.rawmk4
17_16.rawmk4	17_37.rawmk4	17_57.rawmk4	18_18.rawmk4	c18_25.rawmk4
17_19.rawmk4	17_40.rawmk4	18_00.rawmk4	18_21.rawmk4	c18_31.rawmk4
17_22.rawmk4	17_43.rawmk4	18_03.rawmk4	18_28.rawmk4	c18_37.rawmk4
17_25.rawmk4	17_46.rawmk4	18_06.rawmk4	18_34.rawmk4	